

PT32L/F007 Series

SELECTION GUIDE



Deep sleep mode current < 1μA

Part Number	Memory		Core	CPU Frequency	Operating Voltage	GPIO	Timer & Counter				Communication Interface				Clock		Analog		Security	Operating Temperature	SWD Pins	Package Information			
	Flash (K)	RAM (K)		MHz	VDD (V)	I/O	Adv	Base	Low Power	WDG	UART	I2C	SPI	HSI (MHz)	LSI (KHz)	ADC 12-bit	PVD	CRC	°C	Package Types		Thickness	Pin Pitch	Packing	
PT32F007F8P7	64	16	M0	64	2.2-5.5	17+1	1	2	-	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	8, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32F007F8X7	64	16	M0	64	2.2-5.5	17+1	1	2	-	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	8, 18	QFN20(3*3)	0.75mm	0.4mm	Tray	
PT32F007F6P7	32	8	M0	64	2.2-5.5	17+1	1	2	-	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	8, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32F007F6X7	32	8	M0	64	2.2-5.5	17+1	1	2	-	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	8, 18	QFN20(3*3)	0.75mm	0.4mm	Tray	
PT32L007F8P7	64	16	M0	64	1.8-5.5	17+1	1	2	1	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	17, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32L007F8X7	64	16	M0	64	1.8-5.5	17+1	1	2	1	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	14, 15	QFN20(3*3)	0.75mm	0.4mm	Tray	
PT32L007F6P7	32	8	M0	64	1.8-5.5	17+1	1	2	1	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	17, 18	TSSOP20	1.2mm	0.65mm	Tube	
PT32L007F6X7	32	8	M0	64	1.8-5.5	17+1	1	2	1	1	2	1	1	48	32	12ext + 2int	✓	✓	-40~105	14, 15	QFN20(3*3)	0.75mm	0.4mm	Tray	